

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 7,386,711

Confirmation No. 4483

Issued: June 6, 2008

Name of Patentee: Haimovsky et al.

Patent Title: METHOD AND APPARATUS FOR
REDIRECTING THE BOOT OPERATIONS OF
ONE OR MORE SYSTEMS

**REQUEST FOR CERTIFICATE OF CORRECTION OF
PATENT FOR PATENT OFFICE MISTAKE (37 C.F.R. § 1.322)**

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is requested that a Certificate of Correction be issued to correct Office mistakes found the above-identified patent. Attached hereto is a Certificate of Correction which indicates the requested correction. For your convenience, also attached are copies of selected pages (a) from the issued patent with errors annotated, and (b) Amendment F filed October 31, 2007, with the correct text/instructions.

It is believed that there is no charge for this request because applicant or applicants were not responsible for such error, as will be apparent upon a comparison of the issued patent with

In re US Patent No. 7,386,711

the application as filed or amended. However, the Assistant Commissioner is hereby authorized to charge any fee that may be required to Deposit Account No. 501430.

Respectfully submitted,
The Law Office of Kirk D. Williams

Date:

1/20/2010

By



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CERTIFICATE OF CORRECTION

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PATENT NO. : 7,386,711
APPLICATION NO. : 10/042,846
DATED : June 10, 2008
INVENTOR(S) : Haimovsky et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col 12, line 15, Claim 27, replace "apparatus of claim 24" with – apparatus of claim 23 –

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23. An apparatus comprising:
 a master system including: a master memory and a storage
 mechanism for storing a first remote boot image; and a
 master system controller;
 a first slave system including: a first memory, a first
 processor, and a first programmable interface coupled
 to the master system, the first programmable interface
 including a first system controller; and
 wherein the master system is configured to interrogate,
 after the master system is booted and the first system
 controller is released, the first slave system to identify
 one or more characteristics of the first slave system;
 wherein the master system is configured to determine
 that the first remote boot image should be used for the
 first slave system in response to said interrogation of
 the first slave system and based on said identified one
 or more characteristics of the first slave system, to
 update the first programmable interface to cause the
 first slave system to retrieve the first remote boot
 image, and to release the first processor for booting
 from the first remote boot image after updating the first
 programmable interface;
 wherein said operation of updating the first programmable
 interface includes: the master system assigning the first

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system controller an address or range of addresses to be
 used by the master system for accessing one or more
 registers of the first system controller; and the master
 system manipulating said one or more registers of the
 first system controller and programming the master
 system controller such that a boot address that is part of
 a boot-code range of addresses is redirected for access-
 ing the first remote boot image by the first slave system.

24. The apparatus of claim 23, wherein said one or more
 characteristics include its type.

25. The apparatus of claim 24, wherein said one or more
 characteristics include its version.

26. The apparatus of claim 23, wherein said one or more
 characteristics include its version.

27. The apparatus of claim 24, wherein the first slave
 system includes a boot ROM, and wherein an original boot
 address refers to a location in the boot ROM for retrieving
 a locally stored boot image within the first slave system.

28. The apparatus of claim 23, wherein the master system
 is communicatively coupled to the slave system via a bus.

29. The apparatus of claim 28, wherein the bus is a PCI
 bus.

* * * * *

Should
 be
 claim 23

Now Claim 23

Claim 52 (currently amended): An apparatus comprising:

a master system including: a master memory and a storage mechanism for storing a first remote boot image; and a master system controller;

a first slave system including: a first memory and a first programmable interface coupled to the master system, the first programmable interface including a first system controller; and

wherein the master system is configured to ~~perform one or more operations to identify one or more characteristics of the first slave system after the master system is booted and the first system controller is released by the master system, said operations including interrogating~~ interrogate the first slave system to identify ~~their respective said one or ore~~ characteristics of the first slave system; wherein the master system is configured to determine that the first remote boot image should be used for the first slave system in response to said interrogation of the first slave system and based on said identified characteristics of the first slave system, and to update the first programmable interface to cause the first slave system to retrieve the first remote boot image and to boot from said retrieved first remote boot image; wherein said operation of updating the first programmable interface includes: the master system assigning the first system controller an address or range of addresses to be used by the master system for accessing one or more registers of the first system controller; and the master system manipulating said registers of the first system controller and programming the master system controller such that an address that is part of the boot-code range of addresses is redirected for accessing the first remote boot image by the first slave system.

Claim 53 (previously presented): The apparatus of claim 52, wherein said one or more characteristics include its type.

From Amendment F filed 10-31-2007

In re HAIMOVSKY ET AL., Application No. 10/042,846
Amendment F

Claim 54 (previously presented): The apparatus of claim 53, wherein said one or more characteristics include its version.

Claim 55 (previously presented): The apparatus of claim 52, wherein said one or more characteristics include its version.

Claim 56 (canceled)

Claim 57 (currently amended): The apparatus of claim 56 claim 52, wherein the first slave system includes a boot ROM; and wherein the original boot address refers to a location in the boot ROM for retrieving a locally stored boot image within the first slave system.

*Goes to Claim 52,
now Claim 23*

Claim 58 (new): The apparatus of claim 8, wherein the master system is configured to scan the bus looking for system controllers of slave systems in order to discover the first system controller and the second system controller prior to said operation of interrogating the first and the second slave systems.

Claim 59 (new): The apparatus of claim 58, wherein the bus is a PCI bus.

Claim 60 (new): The apparatus of claim 8, wherein the bus is a PCI bus.

Claim 61 (new): The method of claim 13, wherein the master system is said communicatively coupled to the slave system via a PCI bus.

Claim 62 (new): The apparatus of claim 45, wherein the master system is communicatively coupled to the slave system via a bus.

Claim 63 (new): The apparatus of claim 62, wherein the bus is a PCI bus.

Claim 64 (new): The apparatus of claim 52, wherein the master system is communicatively coupled to the slave system via a bus.

Claim 65 (new): The apparatus of claim 64, wherein the bus is a PCI bus.